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ElectronEconet
; PartNo    00
; Date      13/07/2022
; Revision  02
; Designer  Roland Leurs

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Chip ElectronEconet GAL22V10

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00    A1    A2        A3    A4  A5    A6    A7    nPGFC RnW    ROM13EN GND
INTIN NMIOU NMIENABLE RESET nRD nINTON nSTATID nc1  ROMOE nCSROM nCSADLC VCC

```

EQUATIONS

```

/nRD      = 00 * RnW;
/nCSROM    = /ROMOE + /ROM13EN;
/nCSADLC   = /nPGFC * /A7 * /A6 * A5 */A4 * A3 * /A2;
/nSTATID   = /nPGFC * /A7 * /A6 * A5 */A4 * A3 * A2 * /A1 * RnW * 00;
/nINTON    = /nPGFC * /A7 * /A6 * A5 */A4 * A3 * A2 *  A1 * RnW * 00;
  NMIOU    = NMIENABLE * /INTIN;

```

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NMIENABLE = RESET * /nINTON * /nSTATID * NMIENABLE + RESET * /nINTON * nSTATID +
RESET * nINTON * nSTATID * NMIENABLE;

```