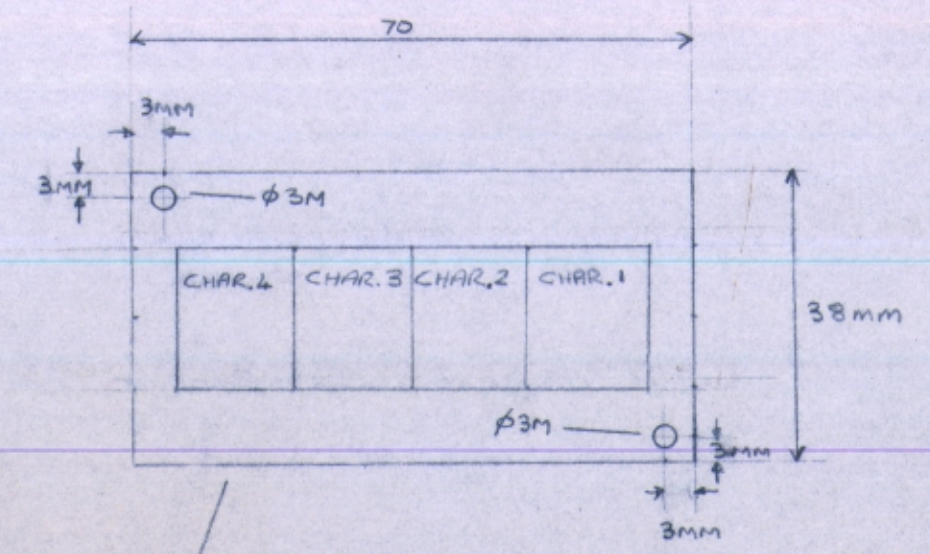
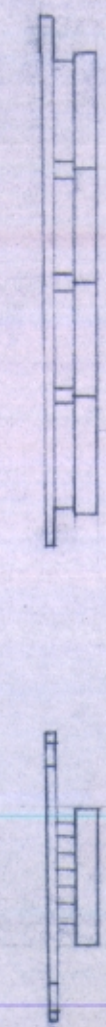


0.1" pitch double sided 11 contacts.

EDGE CONNECTOR AREA
EDGE CONNECTOR TYPE: FARNELL No 104-618
CUT TO 11+11

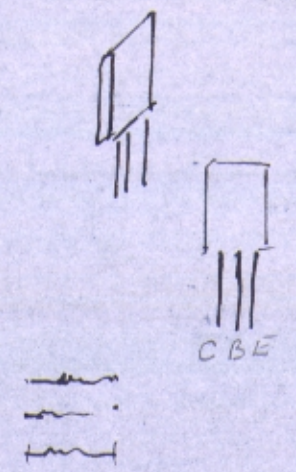


BOARD 2

BLOCK OF FOUR 7x5 DOT MATRIX DISPLAYS TO BE POSITIONED CENTRAL ON BOARD 2.

Edge some details
Transistor - plastic package?
Spring of electrolytic - tantalum beads. (0.2" spacing.)
15 Ω resistors all discrete. $\frac{1}{4}$ W. 0.5" pitch. NS cat. page 150
OK to stand resistors on end if space is short.

3rd part of flight



← actual size

CBE

BRITISH RAILWAYS		SCOTTISH REGION	
CHIEF SIGNAL & TELECOMMUNICATIONS		GLASGOW	
ENGINEER'S DEPARTMENT.			
GENERAL PCB LAYOUTS FOR FOUR CHARACTER DISPLAY			
PLAN DATE	APPROVED BY	CORRES. REF.	
PRODUCED BY		SCALE-	
TRACED BY			
CHECKED BY	CHIEF SIGNAL & TELECOMMUNICATIONS ENGINEER	PLAN No. SES 81/6	
DATE ISSUED		SHEET 2 OF 2	