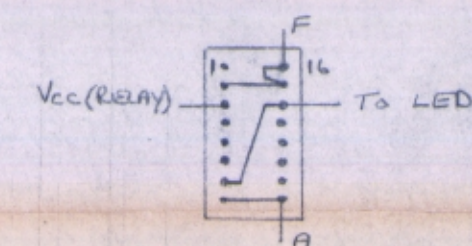


DEVICE No	GND	Vcc	CAPACITOR VALUE
74C 374	10	20	10nF 0.1" PITCH
74C 32	7	14	10nF 0.1" "
74C 14	7	14	10nF 0.1" "
DM8131	8	16	100nF 0.2" "
ULN 2003	8	9	100nF 0.2" "



PCB TRACK LAYOUT  
FOR RELAY BASE  
16 PIN DIL

BRITISH RAILWAYS  
CHIEF SIGNAL & TELECOMMUNICATIONS  
ENGINEER'S DEPARTMENT.

SCOTTISH REGION  
GLASGOW

Sc.R.T.D.M.  
OUTPUT BOARD  
DECOUPLING AND BOARD LAYOUT

PLAN DATE	APPROVED BY	CORRES. REF
PRODUCED BY		SCALE
TRACED BY		NOT TO SCALE
CHECKED BY		PLAN No. 582/3-31
DATE ISSUED	CHIEF SIGNAL & TELECOMMUNICATIONS ENGINEER	SHEET 1 OF 1