

V24
LINK
1-2, 4-5, 8-9, 14-15

TTY
LINK
1-3, 6-7, 8-10,
16-17 TO SOURCE CURRENT
17-18 TO SINK CURRENT

LINK 11-12 IF 'SD'OK
OFF IS REQUIRED TO
TRIGGER 'FAULT'
LINK 12-13 IF NOT

#1 R38, R39 NOT NORMALLY FITTED.
#2 SW1, SW2 NOT NORMALLY FITTED. (SEE BELOW).

- B1 - 74LS04
 - B2 - 74C08
 - B3, B4 - DS3634
 - B5 - CD4538 BCN
 - B6 - 74LS74
 - B7 - 74LS32
 - B8 - 7406
 - B9 - 74125
 - B10 - 74LS02
- SW1 - FARNELL No 145-180
SW2 - FARNELL No 145-182
ALL DIODES IN 4004
1.8432 MHz CRYSTAL - FARNELL 170-226
6 MHz CRYSTAL - FARNELL 103-884
C1 - 0.1µF 35VW TANTALUM 5mm PITCH
C2 - 68µF 10VW TANTALUM 5mm PITCH

CLOCK FREQUENCY LINK MADE BY WIRE-WRAP.
I/O TIE UP/TIE DOWN MADE BY WIRE-WRAP.
TRANSMISSION SELECTION LINKS, AND 'SD'OK TO
'FAULT' LINK, MADE BY R3 SELECTOR PLUG
434-734 IN SOCKET 434-712.
WHEN SW1 NOT FITTED, WIRE LINK STRAPS
'N' POSITION TO 0V.
WHEN SW2 NOT FITTED, WIRE LINK STRAPS
B6 P13 TO 0V.

Revisions
① - MAY '84
(a) ALE made available
at Lc3
(b) Serial data connection
to Fault Ckt now link
selected.
② - MAY '86
a) T-RDY now has direct
link to 'SD'OK delay.
(b) Serial link 2 multi-fid for
I/O use.
(c) OPT2 inserted in current
cct.

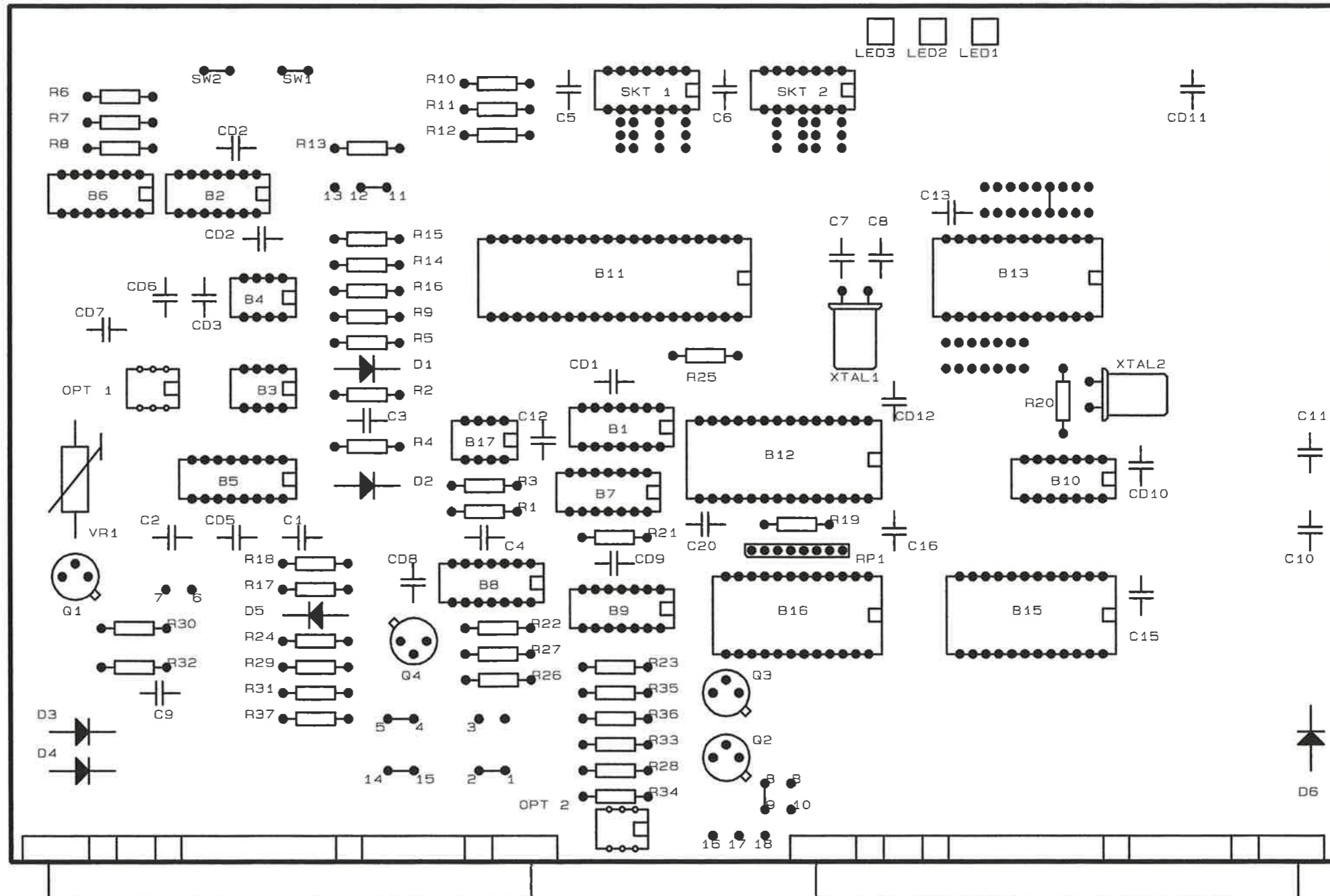
REDRAWN JAN. 89 DUE TO LOSS OF
ORIGINAL NEGATIVE. (P)

British Railways Board
Reg. Signal and Telecommunications Engineer



CONTROL CARD

CARD No	82 SES1/M2
10-3-83	No.



First Projects Ltd		
Title Control Card Overlay		
Size B	Document Number 82SES1 MOD2	REV
Date: November 25, 2005	Sheet 3 of	3