

74LS367

D0	2
D1	4
D2	6
D3	10

EN

From
memory I/O

D4	2
D5	4
D6	6
D7	10

EN

74LS367

3

5

7

9

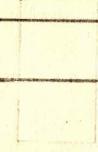
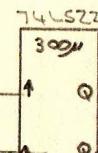
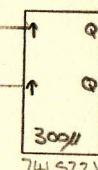
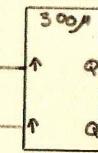
3

5

7

9

74LS221



KB
CONTROLLER

SYNTH. PIN NO.

D0 8

D1 9

D2 10

D3 11

D4 12

D5 13

GATE 7

ENABLE 46

TO DRUM EFFECTS

D0
D1
D2
D3
D4
D5
D6
D7

